

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Xiaohui Qin, Deji Akinwande, James P. Stephens, Robin Zinsmaster, and

Serial No.: unassigned

Examiner:

Filing Date: 10/15/2003

Group Art Unit:

Title: Electrical interconnection for high-frequency devices

COMMISSIONER FOR PATENTS

PO Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or  
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97(c) together with either a:  
☐ Statement under 37 CFR 1.97(e), or  
☐ a \$180.00 Processing fee under 37 CFR 1.17(p), or  
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:  
☐ Statement under 37 CFR 1.97(e), and  
☐ a \$180.00 processing fee under 37 CFR 1.17(p).  
(Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee)

Please charge to Deposit Account **50-1078** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **50-1078** pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449. References identified with an asterisk (\*) were disclosed in Patent Application No. \_\_\_\_\_ filed \_\_\_\_\_, now U. S. Patent No. \_\_\_\_\_, and, as such, copies thereof are not included pursuant to the provisions of 37 CFR 1.98(d).

☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individuals(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

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I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450.

By 

Typed Name: **Scott W. Hewett**

Respectfully submitted,

**Xiaohui Qin, Deji Akinwande, James P.**

By 

**Scott W. Hewett**

Attorney/Agent for Applicant(s)

Reg. No. **41,836**

Date: **10/15/2003**

<b>Form PTO-1449</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (Use several sheets if necessary)	<b>ATTORNEY DOCKET NO.</b> 10030377-1	<b>SERIAL NO.</b>
	<b>APPLICANT</b> Qin et al.	
	<b>FILING DATE</b>	<b>GROUP</b>

REFERENCE DESIGNATION		U.S. PATENT DOCUMENTS		
EXAMINER INITIAL	*	DOCUMENT NUMBER	DATE	NAME

FOREIGN PATENT DOCUMENT					
		DOCUMENT NUMBER	DATE	NAME	TRANSLATION YES NO

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)	
	Sutono et al., Experimental Modeling, Repeatability Investigation and Optimization of Microwave Bond Wire Interconnects, IEEE Transactions on Advanced Packaging, Vol. 24, No. 4, 595-603 (November 2001)
	Alpha Industries, Circuit Models for Plastic Packaged Microwave Diodes, Application Note 1001, 1-4 (June 1999)
	Cherry et al., FDTD Analysis of High Frequency Electronic Interconnection Effects, IEEE Transactions on Microwave Theory and Techniques, Vol. 43, No. 10, 2445-51 (October 1995)
EXAMINER	DATE CONSIDERED

\* Copies of these references are not enclosed Pursuant to 37 CFR 1.98(d). (See accompanying IDS)

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FOREIGN PATENT DOCUMENT					
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)	
	Yun et al., Parasitic Impedance Analysis of Double Bonding Wires for High-Frequency Integrated Circuit Packaging, IEEE Microwave and Guided Wave Letters, Vol. 5, No. 9, 296-98 (September 1995)
EXAMINER	DATE CONSIDERED

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